618	In re a	plication of:)	Attorney Docket No.: NVIDP008B/P000057
AUG	0 8 2005	Lindholm et al.)	Examiner: Unassigned
Street	1.4	on No.: 09/960,004)	Group Art Unit: 2671
	Filed:	September 20, 2001)	Date: June 28, 2002
		MASKING SYSTEM AND METHOD FOR A GRAPHICS PROCESSING FRAMEWORK EMBODIED ON A SINGLE SEMICONDUCTOR PLATFORM)))	
			_)	CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being deposited with the US Patent & Trademark Office via facsimile to fax number (703) 872-9314 op June 28, 2002.

Commissioner for Patents Box Fee Amendment Washington, DC 20231

Sir:

Transmitted herewith is an amendment in the above-identified application.

A copy of this sheet is enclosed for billing purposes.

The fee has been calculated as shown below.

TOTAL	Claims Remaining After <u>Amendment</u>	Highest Previously Paid For Extra	Present	SMALL ENTIT	TY	OR	LARGE ENTITY RATE FEE
TOTAL CLAIMS INDEP	55	22	33	X09 = \$	OR	X18=	\$594
CLAIMS		04		X42 = \$	OR	X84 =	\$420
	pendent Claim Pre Previously Paid	esent		\$130			\$0
	10/10401/1440		TOTAL	\$			\$ <u>1,014.00</u>
	Applicant(s) an extension	believe that no (ad is required, Applic or to charge the rec	lditional) Extensio cant(s) hereby peti	tion that such an ex	ed; howe tension l	ver, if it is be granted	ng Office Action. s determined that such and authorize the to Deposit Account
			_				extension of time fees. e enclosed response,

Respectfully submitted,

K∉yin/J/Zilka

If the required fees are missing or any additional fees are required to facilitate filing the enclosed response, please charge such fees or credit any overpayment to Deposit Account No. 50-1351 (Order No. NVIDP008B).

Registration No. 41,429

P.O. Box 721120

San Jose, CA 95172-1120 Telephone: (408) 971-2573

(Revised 1/96)



PATENT IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:)	Docket: NVIDP008B/P000057
Lindholm et al.)	
Serial No.: 09/960,004)	Examiner: Vo, C.
Filed: September 20, 2001)	
For: MASKING SYSTEM AND)	
METHOD FOR A GRAPHICS)	Date: June 28, 2002
PROCESSING FRAMEWORK)	
EMBODIED ON A SINGLE)	
SEMICONDUCTOR PLATFORM)	
	_)	

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being deposited with the US Patent & Trademark Office via facsimile to fax number (703) 872-9314 on June 28,

Signed:

Erica Mann

PRELIMINARY AMENDMENT C

Commissioner for Patents and Trademarks Washington, DC 20231

Dear Sir:

Please enter the following preliminary amendments to the pending application.

IN THE CLAIMS

Please add claims 48-80 as follows:

48. (New) A graphics pipeline system with an integrated masking operation, comprising:

a transform module positioned on a single semiconductor platform for transforming graphics data;

a lighting module positioned on the same single semiconductor platform as the transform module, the lighting module being for performing lighting operations on the graphics data;

a set-up module positioned on the same single semiconductor platform as the transform module and the lighting module, the set-up module being for setting up the graphics data; and

a rendering module positioned on the same single semiconductor platform as the transform module, the lighting module, and the set-up module, the rendering module being for rendering the graphics data;

wherein a masking operation is capable of being performed utilizing the single semiconductor platform.

- 49. (New) The system as recited in claim 48, wherein the masking operation includes a 2-bit write mask for providing control to a word level of a buffer address.
- 50. (New) The system as recited in claim 48, wherein the masking operation includes individually masking a write operation to at least one register component such that unmasked components are taken from the register and masked components are bypassed.
- 51. (New) The system as recited in claim 48, wherein the masking operation masks an enabled bit of a control vector for allowing analysis of all remaining bits after execution of the masking operation.
- 52. (New) The system as recited in claim 51, wherein the masking operation masks vector vertex data for converting the vector vertex data to scalar vertex data.
- 53. (New) The system as recited in claim 48, wherein the masking operation is associated with an ambient attribute.

- 54. (New) The system as recited in claim 48, wherein the masking operation is associated with a diffuse attribute.
- 55. (New) The system as recited in claim 48, wherein the masking operation is associated with a specular attribute.
- 56. (New) The system as recited in claim 48, wherein the single semiconductor platform is adapted for coupling to a central processing unit for receiving instructions therefrom.
- 57. (New) A method for graphics processing, comprising:
 transforming graphics data from a first space to a second space;
 lighting the graphics data;
 performing a masking operation on the graphics data;
 setting up the graphics data; and
 rendering the graphics data;
 wherein the graphics data is set up, transformed and lighted, and the masking

operation is performed, on a single semiconductor platform.

- 58. (New) The method as recited in claim 57, wherein the masking operation includes a 2-bit write mask for providing control to a word level of a buffer address.
- 59. (New) The method as recited in claim 57, wherein the masking operation includes individually masking a write operation to at least one register component such that unmasked components are taken from the register and masked components are bypassed.
- 60. (New) The method as recited in claim 57, wherein the masking operation masks an enabled bit of a control vector for allowing analysis of all remaining bits after execution of the masking operation.
- 61. (New) The method as recited in claim 60, wherein the masking operation masks vector vertex data for converting the vector vertex data to scalar vertex data.

- 62. (New) The method as recited in claim 57, wherein the masking operation is associated with an ambient attribute.
- 63. (New) The method as recited in claim 57, wherein the masking operation is associated with a diffuse attribute.
- 64. (New) The method as recited in claim 57, wherein the masking operation is associated with a specular attribute.
- 65. (New) A single-platform graphics pipeline system with an integrated masking operation, comprising:
- a transform module positioned on a single semiconductor platform for transforming graphics data;
- a lighting module positioned on the same single semiconductor platform as the transform module, the lighting module being for performing lighting operations on the graphics data;
- a set-up module positioned on the same single semiconductor platform as the transform module and the lighting module, the set-up module being for setting up the graphics data; and
- a rendering module positioned on the same single semiconductor platform as the transform module, the lighting module, and the set-up module, the rendering module being for 3-D rendering of the graphics data;

wherein the single semiconductor platform is adapted for coupling to a central processing unit for receiving instructions therefrom;

wherein a masking operation is capable of being performed utilizing the single semiconductor platform;

wherein the single semiconductor platform is capable of operating with an application program interface.

- 66. (New) The system as recited in claim 65, wherein the masking operation includes a 2-bit write mask for providing control to a word level of a buffer address.
- 67. (New) The system as recited in claim 65, wherein the masking operation includes individually masking a write operation to at least one register component such that unmasked components are taken from the register and masked components are bypassed.

- 68. (New) The system as recited in claim 65, wherein the masking operation masks an enabled bit of a control vector for allowing analysis of all remaining bits after execution of the masking operation.
- 69. (New) The system as recited in claim 65, wherein the masking operation is associated with an ambient attribute.
- 70. (New) The system as recited in claim 65, wherein the masking operation is associated with a diffuse attribute.
- 71. (New) The system as recited in claim 65, wherein the masking operation is associated with a specular attribute.
- 72. (New) A method for graphics processing, comprising:
 transforming graphics data from a first space to a second space;
 lighting the graphics data;
 performing a masking operation on the graphics data;
 setting up the graphics data; and
 3-D rendering the graphics data;

wherein the graphics data is set up, transformed and lighted, and the masking operation is performed, on a single semiconductor platform;

wherein the single semiconductor platform also operates with an application program interface.

- 73. (New) The method as recited in claim 72, wherein the masking operation includes a 2-bit write mask for providing control to a word level of a buffer address.
- 74. (New) The method as recited in claim 72, wherein the masking operation includes individually masking a write operation to at least one register component such that unmasked components are taken from the register and masked components are bypassed.

- 75. (New) The method as recited in claim 72, wherein the masking operation masks an enabled bit of a control vector for allowing analysis of all remaining bits after execution of the masking operation.
- 76. (New) The method as recited in claim 72, wherein the masking operation is associated with an ambient attribute.
- 77. (New) The method as recited in claim 72, wherein the masking operation is associated with a diffuse attribute.
- 78. (New) The method as recited in claim 72, wherein the masking operation is associated with a specular attribute.
- 79. (New) The method as recited in claim 72, wherein the single semiconductor platform is adapted for coupling to a central processing unit for receiving instructions therefrom.
- 80. (New) A single-platform graphics pipeline system with an integrated masking operation, comprising:
- a transform module positioned on a single semiconductor platform for transforming graphics data;
- a lighting module positioned on the same single semiconductor platform as the transform module, the lighting module being for performing lighting operations on the graphics data;
- a set-up module positioned on the same single semiconductor platform as the transform module and the lighting module, the set-up module being for setting up the graphics data;
- a rendering module positioned on the same single semiconductor platform as the transform module, the lighting module, and the set-up module, the rendering module being for 3-D rendering of the graphics data; and

memory positioned on the same single semiconductor platform as the transform module, the lighting module, the set-up module, and the render module for storing the graphics data;

wherein the single semiconductor platform is adapted for coupling to a central processing unit for receiving instructions therefrom;

wherein a masking operation is performed utilizing the single semiconductor platform;

wherein a scissor operation is performed utilizing the single semiconductor platform;

wherein a clipping operation is performed utilizing the single semiconductor platform;

wherein the graphics data is blended utilizing the single semiconductor platform for blending triangles represented by vertex data associated with the graphics data;

wherein a vertex fog operation is performed on the graphics data utilizing the single semiconductor platform;

wherein the single semiconductor platform operates with a Direct3D application program interface;

wherein the single semiconductor platform also operates with an OpenGL application program interface.

REMARKS

The claims have been amended for clarifying what is claimed in the present application. No new matter has been added.

In the event a telephone conversation would expedite the prosecution of this application, the Examiner may reach the undersigned at (408) 505-5100. If any fees are due in connection with the filing of this paper, then the Commissioner is authorized to charge such fees to Deposit Account No. 50-1351 (Order No. NVIDP008B/P000057). A duplicate copy of the transmittal is enclosed for this purpose.

Respectfully submitted,

Silicon/Valley IP Group, LLC.

Kevin J. Zilka

Registration No. 41,429

P.O. Box 721120

San Jose, CA 95172

Telephone: (408) 505-5100



303 ALMADEN BLVD., #600 SAN JOSE, CA 95110 TELEPHONE (408) 971-2573 FAX (408) 971-4660

FAX COVER SHEET

Date:	June 28, 2002	Phone Number	Fax Number
To:	Examiner Vo		(703) 872-9314
From:	Kevin J. Zilka	,	
Docket N	Io.: NVIDP008B/P000057	Application No	.: 09/960,004
Total Nu	mber of Pages Being Transmitted, Including C	Cover Sheet: 11	
Messag	e:		
Please e	enter this amendment into record for application se	erial number 09/960,004.	·
Thank y	ou,		
Kevin J.	. Zilka		
□ Origi	nal to follow Via Regular Mail $ {f X}$ Original will	Not be Sent	l follow Via Overnight Courier

The information contained in this facsimile message is attorney privileged and confidential information intended only for the use of the individual or entity named above. If the reader of this message is not the intended recipient, you are hereby notified that any dissemination, distribution or copy of this communication is strictly prohibited. If you have received this communication in error, please immediately notify us by telephone (if long distance, please call collect) and return the original message to us at the above address via the U.S. Postal Service. Thank you.

IF YOU DO NOT RECEIVE ALL PAGES OR IF YOU ENCOUNTER
ANY OTHER DIFFICULTY, PLEASE PHONE ______ Erica____
AT (408) 971-2573 AT YOUR EARLIEST CONVENIENCE

HP Fax K1220

Log for SVIPG 408 971 4660 Jun 28 2002 3:23pm

-		-			
1 2	et.	I F2	ınsa	~ti	Λn
La	JL.	110	шэа	CLI	υп

<u>Date Time Type Identification</u> <u>Duration Pages Result</u>

Jun 28 3:20pm Fax Sent 917038729314 3:39 11 OK

TO:Auto-reply fax to 48



Auto-Reply Facsimile Transmission





TO:

Fax Sender at 408 971 4660

Fax Information

Date Received: Total Pages:

6/28/02 6:21:31 PM [Eastern Daylight Time]

11 (including cover page)

ADVISORY: This is an automatically generated return receipt confirmation of the facsimile transmission received by the Office. Please check to make sure that the number of pages listed as received in Total Pages above matches what was intended to be sent. Applicants are advised to retain this receipt in the unlikely event that proof of this facsimile transmission is necessary. Applicants are also advised to use the certificate of facsimile transmission procedures set forth in 37 CFR 1.8(a) and (b), 37 CFR 1.6(f). Trademark Applicants, also see the Trademark Manual of Examining Procedure (TMEP) section 702.04 et seq.

Received Cover Page

======>

28 02 03:20p	SVIPG	409	971 4660	p. 1
	SILICON VAL	LEY IP GROUP, I	10	
		ILI II OKOUF, I	ALC.	
303 ALAAAD San jos	EN BLVD., #600 E. CA 95110	TELEPTO	NE (108) 971-2573 408) 971-4660	
	FA	X COVER SHEET	10a) 771-4000	
Date: Aug 28, 2			-	
		Phone Number	Fax Number	
			(703) 872-9314	
Prom: Kevin J. Z	ARE .			
Docket No.: NV	IDP008B/P000057	Application N	o.: 09/ 960,004	
Total Number of Pag	26 Meing Transmitted, Includ	_		
Message:				~
January .				ł
Please enter this arre	ndrama into record for applica	tion serial member 09/960,004.		
		·		1
Thank you,				1
Marrie Provide				П
Kevin J. Zilka				1
Kevin J. Zilka				
Kevin J. Zilka				,
	Via Regular Mati X <u>Orieina</u>	i will Not be Sant D Original w	ill follow Via Overnight Con	nie
	Via Regular Mati X Orixing	i will Not be Sant	ची follow Vis Overnight Con	Trias
	Vio Regular Maii X Orisina	pill Not be Sent	iil follow Via Overnighs Con	nio
	Vio Arystar Maii X <u>Orixing</u>	I will Not be Sent	ill follow Via Overnight Con	Tries
	Via Azyakar Matii X <u>. Orizins</u>	I mill Not be Sant	iill follow Via Overnight Con	nie
	Via Rezular Mail <u>X Oricina</u>	Lyvill Net ba Sant C Original w	eil fellew Vie Overnight Con	rrier
	Via Reyniar Mail <u>X Oricina</u>	i prill Northu Sant	ell follow Via Overnight Con	rrier
	Vio Regular Mail <u>X Oricina</u>	i pill Ner ha Sant 🔲 Criginal u	iill follow Via Overnighs Con	trie
	Vio Stepular Mail X Orisina	f will Net be Sant Original w	सी follow Via Overnighs Con	nie
Original to fallow				
Original to fallow	of the furtistic message is abovey pr	internation of the state of the	anded only for the use of the individual	ಪ ಆ
Continued to failure The information continued to the co	in the facilities monage is abovey professional and the victorial and colors for professional and colors for	commence of the state of the st	anded only for the use of the individual re-discontinuish, distribution of exp distribution of the behavior of the	ಪ ಆ
Continued to failure The information continued to the co	in this facinitie message is alterny province of the increase	iconomic biometers popular derivers o propieto de confeccione de confederada del referencia por confeccione de confedera de la comunicación de comunicación de comunicación de comunicación de comunicación de confederación de confederación de confederación de confederación de confederación de confederación de confederación de co	anded only for de use of the individual revision of experiments of the contract of the contrac	ಪ ಆ
Contributed to feelfore	in the facilitie message is storney or what of this message is to the viscously by profibilities. If you have received and other the critical interests to an extension of the control of	tricingo i in a creationisti sin remotion (and conception) and confirmation in the strategy central of a single control of a control of	anded only for de use of the individual revision of experiments of the contract of the contrac	ಪ ಆ
Continued to featlow The information continues the continues are continued asserting the continues are continued as a continue are continued as a continued a	in the facilitie message is storney or what of this message is to the viscously by profibilities. If you have received and other the critical interests to an extension of the control of	iconomic biometers popular derivers o propieto de confeccione de confederada del referencia por confeccione de confedera de la comunicación de comunicación de comunicación de comunicación de comunicación de confederación de confederación de confederación de confederación de confederación de confederación de confederación de co	anded only for de use of the individual revision of experiments of the contract of the contrac	ಪ ಆ
Criptuel to failure Original to failure The inference extraction is reference, and above. If the third commission is reference, and a calculation is the commission of the c	in the facilitie message is storney or what of this message is to the viscously by profibilities. If you have received and other the critical interests to an extension of the control of	tricingo i in a creationisti sin remotion (and conception) and confirmation in the strategy central of a single control of a control of	anded only for de use of the individual revision of experiments of the contract of the contrac	ಪ ಆ
Continued to featlow The information continues the continues are continued asserting the continues are continued as a continue are continued as a continued a	in the facilitie message is storney or what of this message is to the viscously by profibilities. If you have received and other the critical interests to an extension of the control of	tricingo i in a creationisti sin remotion (and conception) and confirmation in the strategy central of a single control of a control of	anded only for de use of the individual revision of experiments of the contract of the contrac	ಪ ಆ

